## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Joo-Hyong LEE

Serial No.

09/955,288

Confirm. No.:

9373

Filed:

September 19, 2001

SEP 1 1 200

Group Art Unit: 2815

Examiner: Jose R. Diaz

For:

LATCH-UP RESISTANT CMOS STRUCTURE

ASSISTANT COMMISSIONER FOR PATENTS Washington, D. C. 20231

Dear Sir:

Transmitted herewith is an Amendment and/or Reply in the above identified application.

No additional fee is required.

Also attached:

The fee has been calculated as shown below:

	NO. OF CLAIMS	HIGHEST PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE .	FEE
Total Claims	20	20	0	x \$18 =	\$0.00
Independent Claims	3	3	0	x \$84 =	\$0.00
If multiple claims newly presented, add \$280.00					
Fee for extension of time					
		TOTAL FEE DUE			\$0.00

[]	Please charge my Dep submitted herewith.	posit Account No.	16-0607 in the amount of \$.	An additional copy of this transmittal sheet is
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A check in the amount of \$ \_\_\_\_ (Check #\_\_\_\_) is attached. []

The Commissioner is hereby authorized to charge payment of any fees associated with this communication or credit any [X]overpayment, to Deposit Account No. <u>16-0607</u>, including any filing fees under 37 C.F.R. 1.16 for presentation of extra claims and any patent application processing fees under 37 C.F.R. 1.17.

> Respectfully submitted, FLESHNER & KIM, LLP

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